

REMARKS

In the pending office action, the Examiner cites no patentability rejections for pending claims 1 – 45. However, the Examiner rejects claims 37 – 38 under 35 U.S.C. §112, 2nd paragraph, for being indefinite. Specifically, the Examiner asserts that the language in the combining step of claim 37 does not clearly recite what data or sequences are combined and how the sequences of bits are generated. As amended, claim 37 now clearly indicates that the plurality of sequences of bits are generated in the demodulation step and that the combining step combines one of the plurality of sequences of bits with a portion of stored bits associated with the first data block. As amended, claim 37 is clear and satisfies the requirements of §112, 2nd paragraph. Therefore, Applicants respectfully request reconsideration.

The Examiner also rejected claim 37 because “a portion of stored data bits” in line 6 of claim 37 lacks antecedent basis. Applicants note that the portion of stored data bits is claimed as “a portion” of stored data bits (emphasis added), and that neither “the” nor “said” is used anywhere in the phrase “a portion of stored data bits.” As such, the language “a portion of stored data bits” is clearly being used to introduce the portion of stored data bits. Therefore, no additional antecedent basis is required. Applicants respectfully request reconsideration.

Applicants also amended claims 23 and 27 to correct minor typographical errors. No new matter was added.

In light of the above remarks and amended claims, Applicants submit that claims 1 – 45 of the pending application stand in condition for allowance. As such, Applicants respectfully request the Examiner reconsider the rejections and allow claims 1 – 45. Should any issues

remain unresolved, Applicants respectfully request the Examiner call the undersigned so that such issues may be resolved expeditiously.

Respectfully submitted,

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By:

A handwritten signature in cursive script, appearing to read "Jennifer K. Stewart".

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